

**PATENT APPLICATION
IN THE UNITED STATES PATENT AND TRADEMARK OFFICE**

In re application of

Docket No: Q79257

Katsuki KUSUNOKI

Appln. No.: 10/581,335

Group Art Unit: 2812

Confirmation No.: 6238

Examiner: Shantanu Pathak

Filed: December 14, 2006

For: METHOD FOR PRODUCTION OF SEMICONDUCTOR CHIP, AND
SEMICONDUCTOR CHIP

PETITION FOR EXTENSION OF TIME UNDER 37 C.F.R. § 1.136

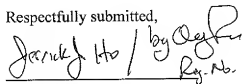

Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Sir:

Pursuant to 37 C.F.R. § 1.136, Applicants hereby petition for an extension of time of one month, extending the time for responding to the Office Action of June 24, 2009 to October 24, 2009, thus providing for timely filing by Monday, October 26, 2009 (October 24, 2009 falls on a Saturday).

The USPTO is directed and authorized to charge the statutory fee of \$130.00 and all required fees, except for the Issue Fee and the Publication Fee, to Deposit Account No. 19-4880. Please also credit any overpayments to said Deposit Account.

Respectfully submitted,

 by 
Reg. No. 33,276

Jerick J. Ho
Registration No. 63,763

SUGHRUE MION, PLLC
Telephone: (202) 293-7060
Facsimile: (202) 293-7860

WASHINGTON OFFICE

23373

CUSTOMER NUMBER

Date: October 26, 2009